

EAST - [09656172.wsp:1]

File View Edit Tools Window Help

☐ Drafts  
☐ Pending  
☒ Active

- ☒ L1: (1399) upconverter
- ☒ L2: (617) 1 and mixer
- ☒ L3: (524) 2 and amplifier
- ☒ L4: (1) 3 and (DC near5 "power control")
- ☒ L5: (85) 3 and inductor
- ☒ L6: (53) 5 and resistor
- ☒ L7: (9) 6 and MESFET
- ☒ L8: (4) 7 and diode
- ☒ L9: (3) 8 and parallel
- ☒ L10: (15) 3 and (RF and MESFET)
- ☒ L11: (9) 10 and (resistor and inductor)
- ☒ L12: (8) 11 and transistor
- ☒ L13: (3) 12 and diode
- ☒ L14: (3) 13 and DC
- ☒ L15: (0) 14 and "gilbert mixer"
- ☒ L16: (9) upconverter and "gilbert mixer"

☐ Failed  
☒ Saved  
☐ Favorites

Search

DB: USPAT:USPGPUB:EPD ☒ Quick

Default operator: OR ☒ Highlight all items initially

upconverter and "gilbert mixer"

BRN form ICR form Image Text HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current X	Retn	Inventor	S
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20010018334 A1	20010830	5	Upconverter mixer circuit	455/318	455/323; 455/333		Ipek, Mehmet et al.	<input checked="" type="checkbox"/>
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6631257 B1	20031007	15	System and method for a mixer circuit with anti-series transistors	455/333	327/355; 327/356;		White, William A. et al.	<input checked="" type="checkbox"/>
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6529721 B1	20030304	8	Low-noise mixer and method	455/323	455/326; 455/333		Tiller, Samuel Alfred	<input checked="" type="checkbox"/>

Ready NUM

EAST - [Untitled1:1]

FileViewEditToolsWindowHelp

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Failed

Saved

Favorites

Tagged (1)

UDC

Queue

Trash

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DBUSPATAUSIPGRUBREPO

Default operatorOR

3 and (DC near 5 "power control")

BRS form

ISA form

Image

Text




















HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current X	Ret	Inventor	S
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6625222 B1	20030923	16	Apparatus and method for high-speed wireless upstream data tra	375/259	725/111		Bertonis, James G. et al.	<input checked="" type="checkbox"/>

HTML

Ready

NUM

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  -  L7: (9) 6 and MESFET
  -  L8: (4) 7 and diode
  -  L9: (3) 8 and parallel
-  Failed
-  Saved
-  Favorites
-  Tagged (1)
-  UDC
-  Queue
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DB: USPAT:US:PGPUB:EPO

☒ Plurals

Default operator: **OR**

☒ Highlight all hit terms initially

8 and parallel

 BRS form
  IS&R form
  Image
  Text
  HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current X	Reti	Inventor	S
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20010036818 A1	20011101	6	Printed circuit doubly balanced mixer for upconverter	455/326	455/333		Dobrovohny, Pierre	<input checked="" type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 5625307 A	19970429	26	Low cost monolithic gallium arsenide upconverter chip	327/113	327/122; 327/565;		Scheinberg, Norman R.	<input checked="" type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 5563545 A	19961008	24	Low cost monolithic GaAs upconverter chip	327/389	327/390; 327/427;		Scheinberg, Norman R.	<input checked="" type="checkbox"/>